

Sir Syed University of Engineering & Technology (SSUET)

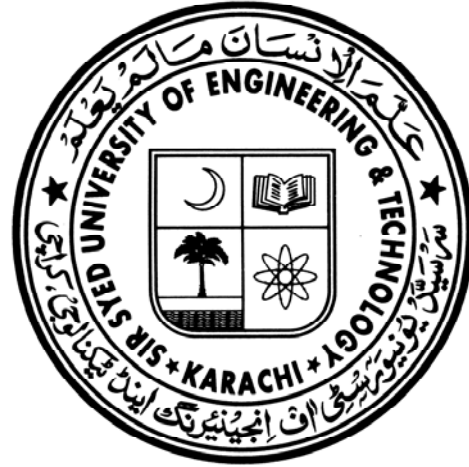
Computer Engineering Department

Course Name: Basic Electronics

Semester: Spring 2011, 3rd

Batch: 2010(Sections: A,B)

Assignments # 7



Course Responsible

Syed Hassan Raza Naqvi

Assistant Professor,
Computer Engineering Department
Office: STI,

SIR SYED UNIVERSITY OF ENGINEERING AND TECHNOLOGY
COMPUTER ENGINEERING DEPARTMENT
BASIC ELECTRONICS
2010 BATCH (Sections: A, B)
Last Submission Date: 25th May, 2011.

Assignment # 7

1. Emitter Bias: Q6 – to – Q10
2. Voltage Divider Bias: Q12 – to – Q19
3. DC Bias with Voltage Feedback: Q22 – to – Q26
4. Miscellaneous Bias Configurations: Q27 – to – 31
5. Design Operation: Q32 – to – Q35